



The diagram illustrates a 1000-bit bus system architecture. It is divided into three main functional blocks: the 1000-BIT BUS, the 1000-BIT CONTROL BUS, and the 1000-BIT I/O BUS. Each block contains a 1000-bit data bus, a 1000-bit address bus, and a 1000-bit control/status bus. The 1000-bit data bus is connected to the 1000-bit data bus in the 1000-BIT I/O BUS. The 1000-bit address bus is connected to the 1000-bit address bus in the 1000-BIT I/O BUS. The 1000-bit control/status bus is connected to the 1000-bit control/status bus in the 1000-BIT I/O BUS. The 1000-bit I/O BUS is connected to the 1000-bit I/O BUS. The diagram shows the interconnections of these buses and the resulting 1000-bit data bus, address bus, control/status bus, and I/O bus.