



The diagram illustrates a complex 1000-bit data bus architecture. It features three main horizontal sections, each representing a different component or stage of the system. The top section, labeled "1000-BIT DATA BUS", shows a series of 1000 data lines (D0 to D999) connected to a "DATA BUS" block. The middle section, also labeled "1000-BIT DATA BUS", shows a series of 1000 data lines (D0 to D999) connected to a "DATA BUS" block. The bottom section, labeled "1000-BIT DATA BUS", shows a series of 1000 data lines (D0 to D999) connected to a "DATA BUS" block. The diagram includes various control signals, address lines, and data lines, with a color-coded legend at the bottom.