



The diagram illustrates a 1000-bit data bus architecture. It features three main horizontal sections: a top section for the Data Bus, a middle section for the Control Bus, and a bottom section for the Address Bus. Each section contains a long horizontal line with 1000 individual lines, each labeled with a number from 0 to 999. The Data Bus section is labeled 'Data Bus' and the Control Bus section is labeled 'Control Bus'. The Address Bus section is labeled 'Address Bus'. On the right side of the diagram, there is a block labeled 'Memory'. The diagram is enclosed in a rectangular frame with a title bar at the top.