



The diagram illustrates a 1000-bit data bus system. It is divided into three main sections: Data Bus, Control Bus, and Address Bus. Each section contains a long horizontal bar representing the bus, with individual bit positions numbered from 1 to 1000. The Data Bus section is at the top, the Control Bus is in the middle, and the Address Bus is at the bottom. Each section is connected to a central vertical line, which in turn connects to a series of small rectangular blocks representing data elements or memory locations. The diagram is color-coded, with different colors used for different sections and components.